

Session F

Microwave Integrated Circuits

Chairman:

A.P.S. Khanna

Avantek Inc.

M/S M20, 481 Cottonwood Drive
Milpitas, CA

This session consists of seven papers representing novel ideas in Microwave Integrated Circuits. The lead paper describes a broadband 4-8 GHz phase shifter using all-pass networks. The next two papers relate to new techniques in the development of frequency multipliers using both bipolar and diode technologies. A non-linear design of a HEMT harmonic oscillator is described next, followed by a paper on V-band FET DROs. A new approach to an electronically tunable equalization using MMIC technology is presented next. The last paper describes a 0 to 12 GHz hybrid matrix distributed amplifier using HEMTs which achieves low power consumption and over 20 dB of gain. These papers were selected from an unusually large number of submitted papers, reflecting a very high level of interest and technical content.

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1:30 p.m.-3:00 p.m., Tuesday, June 11, 1991
Ballroom B